

**Bangladesh Army University of Engineering and Technology (BAUET)**  
**Department of Computer Science and Engineering (CSE)**  
**Qadirabad, Natore**  
**[Effective from 28/03/2021]**

**11<sup>th</sup> Batch**

2 <sup>nd</sup> Year 1 <sup>st</sup> Semester	Day	08:15 AM – 09:10 AM	09:15 AM – 10:10 AM	10:15 AM – 11: 10 AM	11:10 AM – 11:40 AM	11:40 AM – 12:35 PM	12:40PM – 01:35 PM	01:40PM – 02:35 PM	
	SUN	<b>Class Test</b>	CSE-2105:[DT] (P- 605)	MATH-2145:[MAAM] (P- 605)	<b>TEA BREAK</b>	[CSE-2104[AS+MHR](LAB-204)Section-A / [CSE-2106(DT+ASP)(LAB-210)Section-B]			
	MON	<b>FALL IN</b>	CSE-2105:[DT] (P- 605)	CSE-2101:[MRH] (P- 605)		CSE-2103:[MHR] (P- 605)	CSE-2103:[AS] (P- 605)	CSE-2105:[ASP] (P- 605)	
	TUE	[CSE-2102[AY+ASP](LAB-304)Section-A/B]				CSE-2103:[AS] (P- 605)	MATH-2145:[MAAM] (P- 605)	EEE-2163:[SKS] (P- 605)	
	WED	<b>Class Test</b>	CSE-2101:[MRH] (P- 605)	CSE-2101:[MRH] (P- 605)		MATH-2145:[MAAM] (P- 605)	EEE-2163:[SKS] (P- 605)	EEE-2163:[SKS] (P- 605)	
	THU	[EEE-2164[MSH] LAB- Circuit Lab)Section-A/B]				[CSE-2104[AS+MHR](LAB-209)Section-B] / [CSE-2106(DT+ASP)(LAB-206)Section-A]			

**Teachers Name**

**MRH:** Mirza A.F.M. Roshidul Hasan  
**AY:** Asma Yasmin  
**AS:** Ananya Satrker  
**DT:** Dardina Tasmere  
**MHR:** Md. Harun Or Rashid  
**MAAM:** Md.Abdulla Al Mamun  
**ASP:** Aroni Saha Prapti  
**SKS:** Shuvojit Kumar Singha

Course Code      Course Title  
CSE-2101      : Digital Logic Design  
CSE-2102      : Digital Logic Design Sessional  
CSE-2103      : Data Structures and Algorithms-I  
CSE-2104      : Data Structures and Algorithms-I Sessional  
CSE-2105      : Object Oriented Programming  
CSE-2106      : Object Oriented Programming Sessional  
EEE-2163      : Electronic Devices and Circuits  
EEE-2164      : Electronic Devices and Circuits Sessional  
MATH-2145      : Vector Analysis, Matrices and Fourier Analysis

Mohammed Golam Sarwar Bhuyan  
Associate Professor & Head  
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